Bhavans Vivekananda College of Science Humanities & Commerce
Department of Physics & Electronics
2017-18

Department has conducted an extension lecture on "**Digital system design using VHDL**" by **Mr G. Sridhar**, Head Dept. of Electronics, HRD Degree and PG college, Hyderabad on 27 – 11 – 2017.

The objective of the guest lecture is to introduce VHDL programming and to motivate the students to design digital circuits using VHDL programming. Mr.Sridhar gave introduction to VHDL and explained different modeling styles, associated libraries and design flow of few circuits like adders and subtractors S.Allen from III yr on behalf of students proposed vote of thanks and expressed gratitude towards guests for sparing his valuable time. He also thanked Principal & Management and Department of Physics & Electronics for arranging such a knowledgeable lecture.

The Department of Physics and Electronics, Bhavan's Vivekananda College, Sainikpuri organized **an Outreach Programme - 2017** for the students of class IX and X, Zilla Parishath High School, Yapral on 16 – 09 – 2017 The event was conducted successfully by the students of B.Sc. with Physics/Electronics guided by two faculty members Mrs. P. Lavanya and Mrs. T Sai Santoshi. The program included visual and oral presentations and experimental demonstrations by the students in selected topics of Physics.

506/BVC/Tour/2017-18   16th Nov 2017

To
Principal
Bhavans Vivekananda College
of Science Humanities and Commerce
Sainikpuri, Secunderabad

<u>Report-TRIP TO SRIHARIKOTA</u>

Respected sir,

      Department of Physics and Electronics of Bhavan's Vivekananda College, Sainikpuri ~~has~~ organized an **educational trip toSatish Dhawan Space Centre (SDSC) SHAR, Shriharikota,**Nellore District, Andhra Pradesh from 9th Nov to 12thNov 2017.This educational tour provided a great opportunity to studentsto learn various concepts and stages involved in launching of rockets.Many thanks to the managements of Bhavan's Vivekananda College and SHAR centre for giving this opportunity for the students.

The tour kicked off on 9thNovember 2017 at 6.00 am from the college by busses. A group of 104 students with four faculty members boarded three busses and headed to Nellore.

The students thoroughly enjoyed the long journey for about 24 hours.After a long and tiring journey, we arrived at Haritha Hotel, AP tourism, all the students checked into the rooms as allotted and relaxed to recover.

The Following Morning, at 5 am (10-11-2017) they checked into their allotted room, refreshed themselves and got ready by 7:30 tovisit Satish Dhawan Space Center one of the best rocket launching centers. The eagerly awaited moment has arrived when we reached SDSC SHAR. We visited the following places**MCC Mission control centre, Static Test & Evaluation Complex (STEX), Telemetry Tracking & Control centre, First Launch Pad (FLP) and Second Launch Pad (SLP).**

After a tiring day enriched with the knowledge involved in rocket science the student were taken back to hotel. The next morning of 11-11-2017 at about 9 am student were taken to local tour to Visit to Sriranganathaswamy temple, Jonnawada, and Mypadu beach. After a quick refresh and dinner at about 9 pm started our journey backto Hyderabad and reached the college next day (12-11-17) 7.30 am.

The trip was a great success proving to be highly educational filled with full of fun and entertainment.The tour to Sriharikota from 9th Nov to 12th Nov 2017 is completed safely and smoothly.The Exotic tours has provided good quality accommodation, neat and hygienic food as per the itinerary mentioned.

      Thank you,

                    Yours sincerely,

                    Dr GSVRK CHOUDARY

8085 Simulator - D:\8085\unit 2\programs\demofile.asm

File Edit Tools Settings Simulation Subroutine View Load Sample Program Help

Editor | Assembler

Assembler

*	Address	Label	Mnemonics	Hexcode	Bytes	M-Cycles	T-States
√	0000		SUB A	97	1	1	4
√	0001		MVI B,04	06	2	2	7
	0002			04			
√	0003		MVI C,03	0E	2	2	7
	0004			03			
√	0005	LOOP1	ADD B	80	1	1	4
√	0006		DCR C	0D	1	1	4
√	0007		JNZ LOOP1	C2	3	3	10
	0008			05			
	0009			00			
√	000A		STA 9000	32	3	4	13
	000B			00			
	000C			90			
√	000D		HLT	76	1	2	5

Simulate

Start From → 0000

Run all At a Time | Step By Step

Registers | Memory | Devices

Registers :

Register	Value	7	6	5	4	3	2	1	0
Accumulator	00	0	0	0	0	0	0	0	0
Register B	00	0	0	0	0	0	0	0	0
Register C	00	0	0	0	0	0	0	0	0
Register D	00	0	0	0	0	0	0	0	0
Register E	00	0	0	0	0	0	0	0	0
Register H	00	0	0	0	0	0	0	0	0
Register L	00	0	0	0	0	0	0	0	0
Memory(M)	00	0	0	0	0	0	0	0	0

Resister	Value	S	Z	*	AC	*	P	*	CY
Flag Resister	00	0	0	0	0	0	0	0	0

Type	Value
Stack Pointer(SP)	0000
Memory Pointer (HL)	0000
Program Status Word(PSW)	0000
Program Counter(PC)	0000
Clock Cycle Counter	0
Instruction Counter	0

SOD	SID	INTR	TRAP	R7.5	R6.5	R5.5
0	0	0	0	0	0	0

For SIM instruction

SOD	SDE	*	R7.5	MSE	M7.5	M6.5	M5.5
0	0	0	0	0	0	0	0

For RIM instruction

SID	I7.5	I6.5	I5.5	IE	M7.5	M6.5	M5.5
0	0	0	0	0	0	0	0

No. Converter Tool :

Hexadecimal	Decimal	Binary
0	0	0

Created by : Jubin Mitra

Jubin Mitra 8085 simulator for the lab experiments demonstrations…
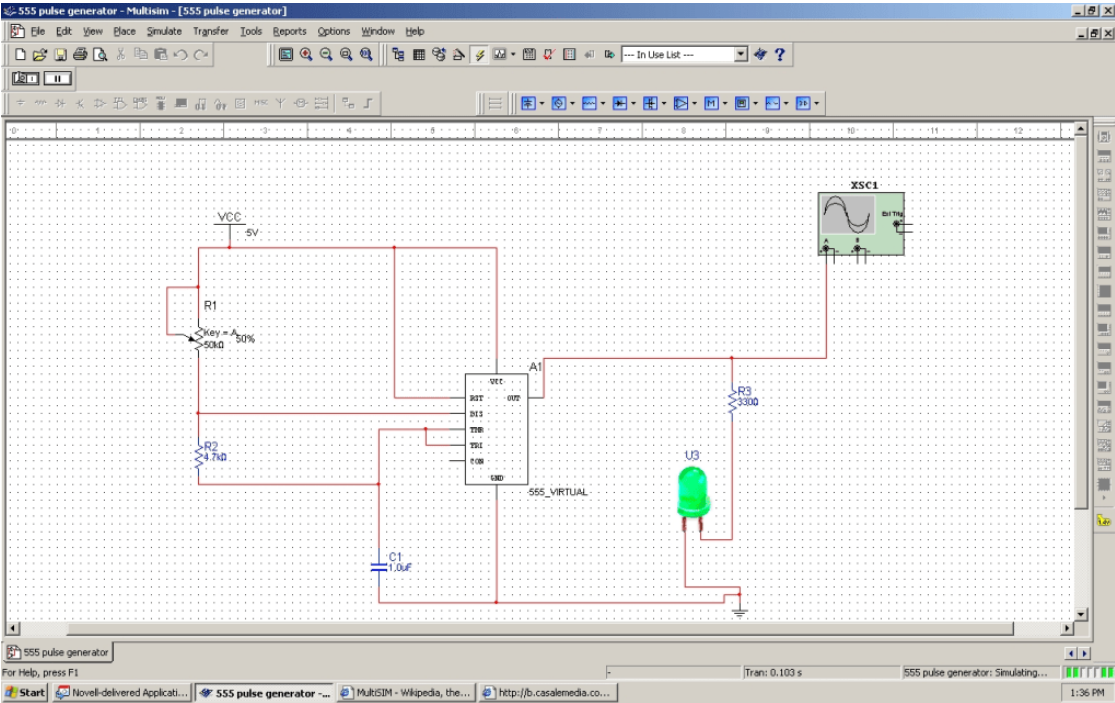
PhET

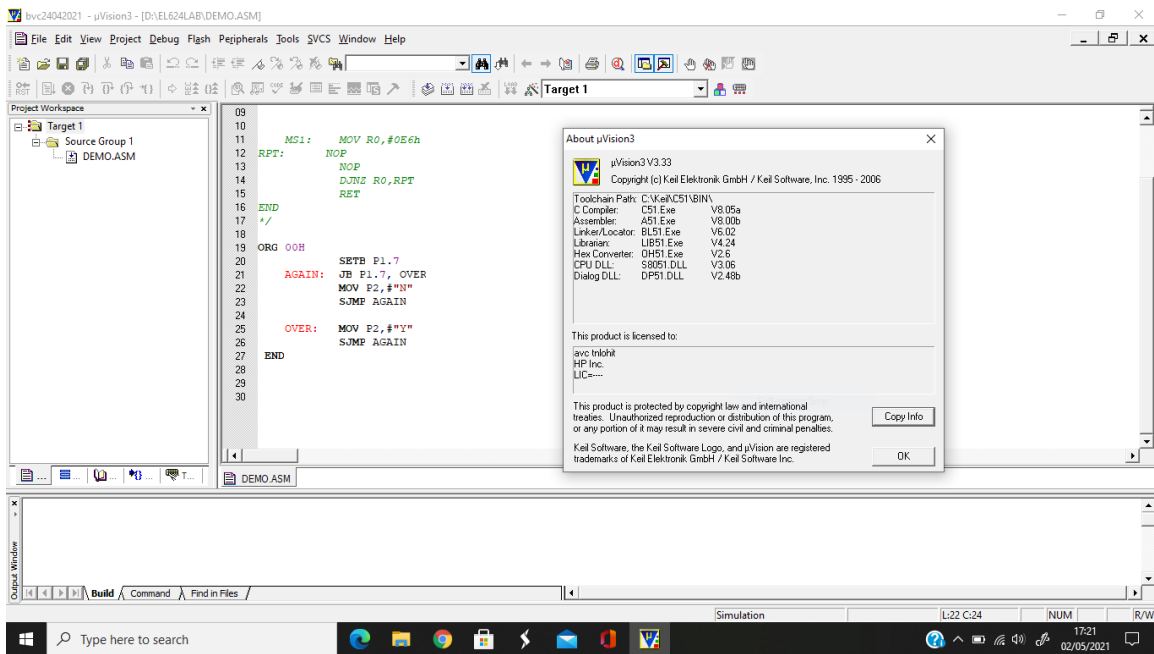# Deeds Digital Circuit Simulator
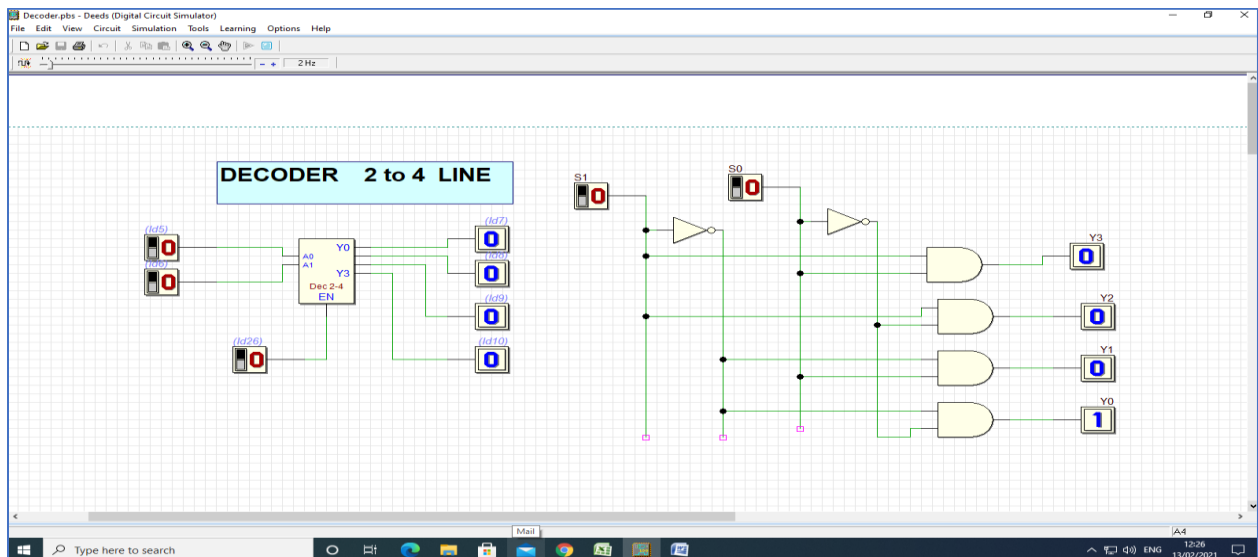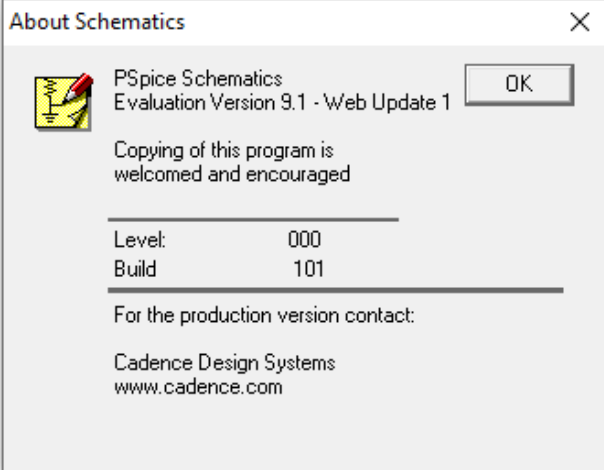
Pspice schematics 9.1 used for lab experiments

NI Multisim is an electronic schematic capture and simulation program which is part of a suite of circuit design programs, along with NI Ultiboard. Multisim is one of the few circuit design programs to employ the original Berkeley SPICE based software simulation.

keil microvision 3 v3.33